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TRANSMITTAL FORM

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Total Number of Pages in This Submission

Application Number	10/812,525
Filing Date	March 29, 2004
First Named Inventor	Stephen M. Trimberger
Art Unit	2819
Examiner Name	Vibol Tan
Attorney Docket Number	X-1006-2C US

ENCLOSURES (Check all that apply)

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Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	XILINX, INC. CUSTOMER NO. 24309		
Signature			
Printed name	Kim Kanzaki		
Date	December 1, 2005	Reg. No.	37,652

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

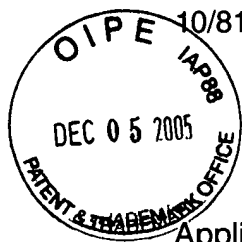
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X-1006-2C US
10/812,525

PATENT
Conf. No.: 3296



IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger
Assignee: Xilinx, Inc.
Title: A Programmable Logic Device With Time-Multiplexed Interconnect
Serial No.: 10/812,525 File Date: 3/29/2004
Examiner: Vibol Tan Art Unit: 2819
Docket No.: X-1006-2C US Conf. No.: 3296

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RESPONSE TO THIRD OFFICE ACTION

Dear Sir:

In response to the Second Office Action mailed from the Patent Office on September 02, 2005, please replace/substitute the following claims as indicated.

Listing of the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.